

REMARKS

Claims 1-18 remain in the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding objections are respectfully requested in light of the following remarks.

The drawings were objected to under 37 CFR 1.83.

The Examiner alleges that the second frequency synthesis circuit including an enable multiplexer having a select input coupled to receive the first input signal (EN1) must be shown.

The Examiner's attention is directed to Figure 9b where this aspect is shown at element 243.

It is respectfully submitted that the drawings are in full compliance with 37 CFR 1.83.

Applicants appreciate the indication that the application is in condition for allowance.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



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